

Home | Login | Logour | Access information | Alc

Welcome United States Patent and Trademark Office

Search Results

EROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "((microarray* <and> processor)<in>metadata)"</in></and>						
	matched 3 of 1222090 documents of 100 results are displayed, 25 to	a page, sorted by Relevance in Descending order.				
Amaximani	or results are displayed, 25 to	a page, solice by relevance in sescending dider.				
o Search Op	tions					
View Session History		Modify Search				
New Search		((mlcroarray* <and> processor)<in>metadata)</in></and>				
» Key		Display Format:				
ieee jnl	IEEE Journal or Magazine					
IEE JNL	IEE Journal or Magazine	Select Article Information				
HEER ONF	IEEE Conference Proceeding					
IEE CNF	IEE Conference Proceeding	Interactively exploring hierarchical clustering results [ger Jinwook Seo; Shneiderman, B.;	e identification]			
IEEE STO	IEEE Standard	Computer				
		Volume 35, Issue 7, July 2002 Page(s):80 - 86 Digital Object Identifier 10.1109/MC.2002.1016905				
		AbstractPlus References Full Text: PDF(1593 KB) IEEE	in.			
		2. Cellular neural networks for real-time DNA microarray and	alysis			
		Arena, P.; Bucolo, M.; Fortuna, L.; Occhipinti, L.; Engineering in Medicine and Biology Magazine, IEEE				
		Volume 21, Issue 2, Mar-Apr 2002 Page(s):17 - 25				
		Digital Object Identifier 10.1109/MEMB.2002.1000180	,			
		AbstractPlus Full Text: PDF(711 KB) 接続性 いれ				
		3. Design of a MEMS acoustical beamforming sensor microa	ırrav			
		Chowdhury, S.; Ahmadi, M.; Miller, W.C.;	•			
		Sensors Journal, IEEE				
		Volume 2, Issue 6, Dec. 2002 Page(s):617 - 627				
		Digital Object Identifier 10.1109/JSEN.2002.807773	•			
		AbstractPlus References Full Text: PDF(1356 KB) ※EEE J	MF.			

indexed by #Inspec* Help Contact Us Privac © Copyright 2006 Hi



Home | Login | Logour | Access information | Alc

Welcome United States Patent and Trademark Office

Searc Searc	n Results

Search Resul	ts			erowse search ieee xplore guide
Your search n	array* <and> processor <and> m natched 19 of 1222090 documents. f 100 results are displayed, 25 to a</and></and>			
s Search Opti	ons .	Modify	u Ses	arcii
View Session History				nd> processor <and> memory <and> grid*)<in> metadata)</in></and></and>
New Search			Checl	k to search only within this results set
» Кеу		Displa	y Fo	remat:
BEEEE JINK.	IEEE Journal or Magazine	S		·
iee jnl	IEE Journal or Magazine	Select	.4	rticle Information
HINE CHE	IEEE Conference Proceeding		1.	Parallel processing approach for finite difference modelling of human thorax as a volume co
iee Cnf	IEE Conference Proceeding	••••		Hyttinen, J.; Arthur, R.M.; Kauppinen, P.; Malmivuo, J.; Engineering in Medicine and Biology society, 1997. Proceedings of the 19th Annual International C
IEEE STO	IEEE Standard			Volume 6, 30 Oct2 Nov. 1997 Page(s):2458 - 2460 vol.6
				Digital Object Identifier 10.1109/IEMBS.1997.756822
				AbstractPlus Full Text: PDE(300 KB) IEEE CNF
			2.	Efficient reconfiguration algorithms for degradable VLSI/WSI arrays Kuo, SY.; Chen, iY.; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on Volume 11, Issue 10, Oct. 1992 Page(s):1289 - 1300
				Digital Object Identifier 10.1109/43.170991 AbstractPlus Full Text: PDF(1044 KB) IMME NR.
			3.	MAP (mobile AGP processor) - a high performance integrated graphics module Pendse, R.; Marcus, B.; Yee, M.H.; Yun, J.S.; Zahn, B.; Jafari, B.; Dewey, T.; Lau, T.; Michael, M.; Electronic Components and Technology, 2004. ECTC '04. Proceedings Volume 1, 1-4 June 2004 Page(s):7 - 16 Vol.1 Digital Object Identifier 10.1109/ECTC.2004.1319308
				AbstractPlus Full Text: PDF(1635 KB) IEEE CNF
			4.	Parallel algorithms for interactive manipulation of digital terrain models Davis, E.W.; McAllister, D.F.; Nagaraj, V.; Frontiers of Massively Parallel Computation, 1988. Proceedings., 2nd Symposium on the Frontiers 10-12 Oct. 1988 Page(s):133 - 136 Digital Object Identifier 10.1109/FMPC.1988.47441
				AbstractPlus Full Text: PDF(244 KB) SEEE CMF
			5.	Multi-chip single package 32 bit floating point digital signal processor with built-in 64 K-byte memory Lin-Hendel, C.G.; Cong, L.H.; Gauntlett, C.H.; Segelken, J.M.; Electronic Components Conference, 1989. Proceedings., 39th 22-24 May 1989 Page(s):636 - 640 Digital Object Identifier 10.1109/ECC.1989.77817
				AbstractPlus Full Text: PDE(516 KB) ISSEE CNF
			6.	A design space evaluation of grid processor architectures

Nagarajan, R.; Sankaralingam, K.; Burger, D.; Keckler, S.W.; Microarchitecture, 2001. MICRO-34. Proceedings. 34th ACM/IEEE International Symposium on 1-5 Dec. 2001 Page(s):40 - 51 Digital Object Identifier 10.1109/MICRO.2001.991104 AbstractPlus | Full Text: PDF(1244 KB) INNEC CNF 7. Multiple scattering among vias in planar waveguides using preconditioned SMCG method Chung-Chi Huang; Leung Tsang; Chi Hou Chan; Kung-Hau Ding; Microwave Theory and Techniques, IEEE Transactions on Volume 52, Issue 1, Part 1, Jan. 2004 Page(s):20 - 28 Digital Object Identifier 10.1109/TMTT.2003.821229 AbstractPlus | References | Full Text: PDF(672 KB) | IEEE JNL 8. A linear array of processors with partially shared memory for parallel solution of PDE Okawa, Y.; Haraguchi, N.; Supercomputing '90. Proceedings of 12-16 Nov. 1990 Page(s):41 - 48 Digital Object Identifier 10.1109/SUPERC.1990.130000 AbstractPlus | Full Text: PDE(452 KB) INNE CNF 9. Communication-minimal mapping of uniform loop nests onto distributed memory architectu Darte, A.; Robert, Y.; Application-Specific Array Processors, 1993. Proceedings., International Conference on 25-27 Oct. 1993 Page(s):1 - 14 Digital Object Identifier 10.1109/ASAP.1993.397116 AbstractPlus | Full Text: PDF(528 KB) IEEE CRF 10. VLSI design of a massively parallel processor Karabernou, S.M.; Mazare, G.; Payan, E.; Rubini, P.; Circuits and Systems, 1991., Proceedings of the 34th Midwest Symposium on 14-17 May 1991 Page(s):839 - 842 vol.2 Digital Object Identifier 10.1109/MWSCAS.1991.252082 AbstractPlus | Full Text: PDF(384 KB) ISEE CNF 11. Lessons learned from porting vector computer applications onto non-uniform memory acce Hatazaki, T.; High Performance Computing and Grid in Asia Pacific Region, 2004. Proceedings. Seventh Interna 20-22 July 2004 Page(s):236 - 243 Digital Object Identifier 10.1109/HPCASIA.2004.1324040 AbstractPlus | Full Text: PDF(390 KB) IEEE CNF 12. A VLSI chip set for a small mainframe processor Yamada, S.; Sano, T.; Saito, M.; Motohashi, M.; Oki, H.; Tsuruya, H.; Custom Integrated Circuits Conference, 1988., Proceedings of the IEEE 1988 16-19 May 1988 Page(s):24.6/1 - 24.6/4 Digital Object Identifier 10.1109/CICC.1988.20929 AbstractPlus | Full Text: PDF(272 KB) ISSE CNF 13. PROTON: a parallel detailed router on an MIMD parallel machine П Yamauchi, T.; Nakata, T.; Koike, N.; Ishizuka, A.; Nishiguchi, N.; Computer-Aided Design, 1991. ICCAD-91. Digest of Technical Papers., 1991 IEEE International Computer-Aided Design, 1991. ICCAD-91. Digest of Technical Papers., 1991 IEEE International Computer-Aided Design, 1991. ICCAD-91. Digest of Technical Papers., 1991 IEEE International Computer-Aided Design, 1991. ICCAD-91. Digest of Technical Papers., 1991 IEEE International Computer-Aided Design, 1991. ICCAD-91. Digest of Technical Papers. 11-14 Nov. 1991 Page(s):340 - 343 Digital Object Identifier 10.1109/ICCAD.1991.185270 AbstractPlus | Full Text: PDF(256 KB) ISSE CNF 14. The first MAJC microprocessor: a dual CPU system-on-a-chip Kowalczyk, A.; Adler, V.; Amir, C.; Chiu, F.; Choon Ping Chng; De Lange, W.J.; Yuefei Ge; Ghosh, Baoqing Huang; Kant, S.; Kao, Y.S.; Cong Khieu; Kumar, S.; Lan Lee; Liebermensch, A.; Xin Liu; N

A.A.; Ngo, H.; Sung-Hun On; Orginos, I.; Shin, L.; Sur, B.; Tremblay, M.; Tzeng, A.; Vo, D.; Zambe Solid-State Circuits, IEEE Journal of Volume 36, Issue 11, Nov. 2001 Page(s):1609 - 1616 Digital Object Identifier 10.1109/4.962280
AbstractPlus References Full Text: PDF(327 KB) ###################################
15. Thin, acoustic lenses applied in 64-beam, 750-kHz diver-held sonar Belcher, E.O.; OCEANS '97. MTS/IEEE Conference Proceedings Volume 1, 6-9 Oct. 1997 Page(s):451 - 456 vol.1 Digital Object Identifier 10.1109/OCEANS.1997.634407 AbstractPlus Full Text: PDE(780 KB)
16. A parallel implementation of an electrothermal simulation for GaAs MESFET devices Chow Sit Tsang-Ping; Snowden, C.M.; Barry, D.M.; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on Volume 15, Issue 3, March 1996 Page(s):308 - 316 Digital Object Identifier 10.1109/43.489101 AbstractPlus References Full Text: PDF(916 KB) \$\$\frac{150}{250}\$ \$\frac{150}{250}\$ \$\fra
17. Coprocessor design for multilayer surface-mounted PCB routing Venkateswaran, R.; Mazumder, P.; Very Large Scale Integration (VLSI) Systems, IEEE Transactions on Volume 1, Issue 1, March 1993 Page(s):31 - 45 Digital Object Identifier 10.1109/92.219905 AbstractPlus Full Text: PDF(1272 KB) IEEE JNL
18. Reducing the cost of Bluetooth systems Phillips, M.; Electronics & Communication Engineering Journal Volume 13, Issue 5, Oct 2001 Page(s):204 - 208 AbstractPlus Full Text: PDF(955 KB) REGUNG
19. ICCAD 2004. International Conference on Computer Aided Design (IEEE Cat. No.04CH37606) Computer Aided Design, 2004. ICCAD-2004. IEEE/ACM International Conference on 7-11 Nov. 2004 Full Text: PDE(401 KB)

#Inspec

Help Contact Us Privac

© Copyright 2005 i€